

FIG. 1

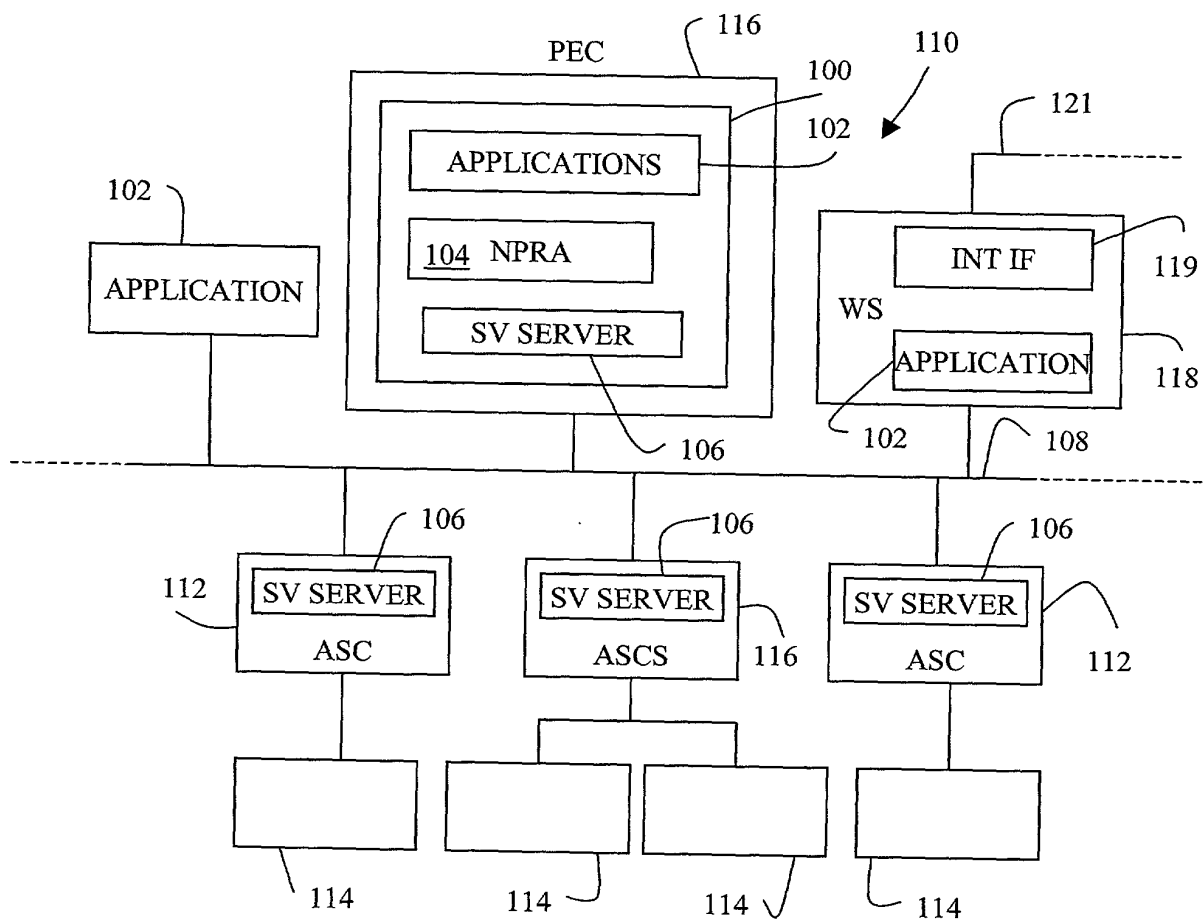


FIG. 2

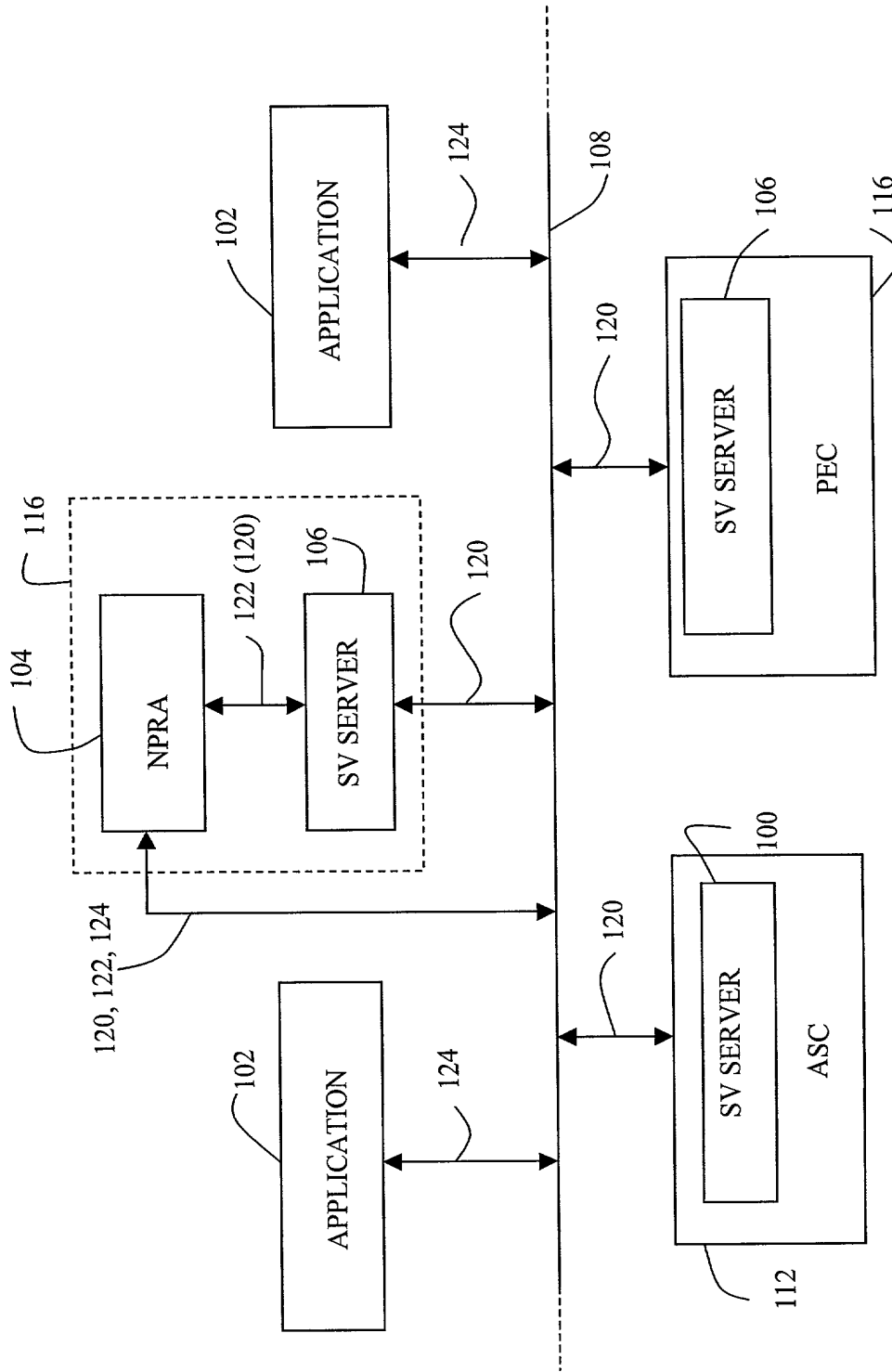


FIG. 3

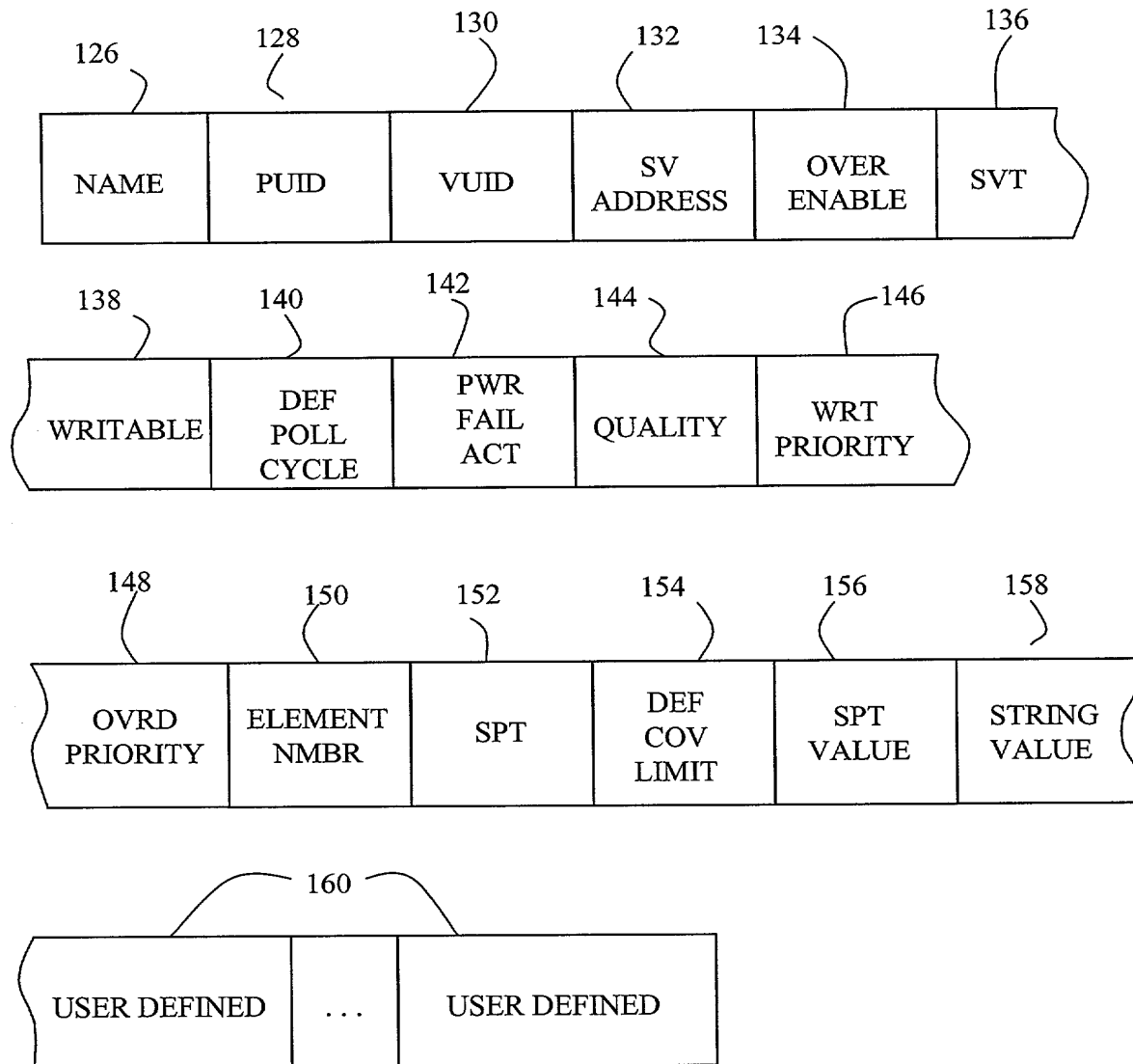


FIG. 4

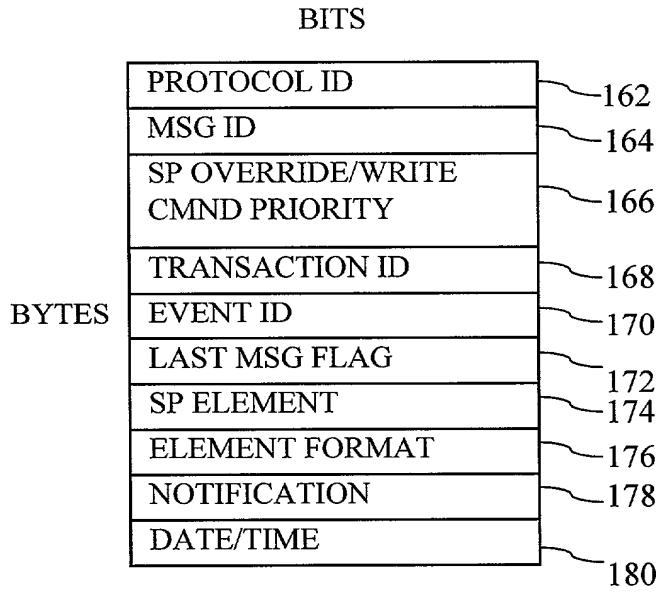


FIG. 5

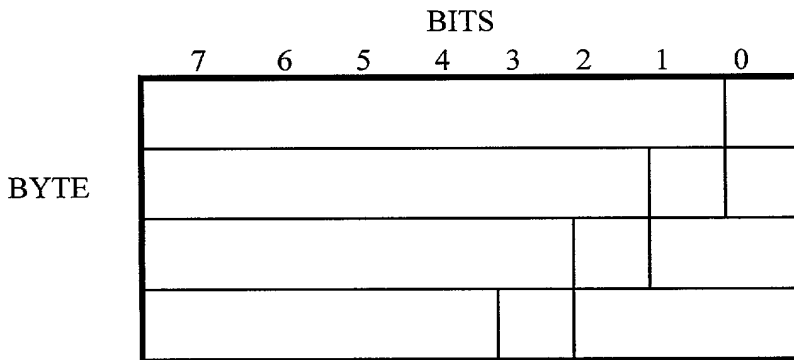


FIG. 6

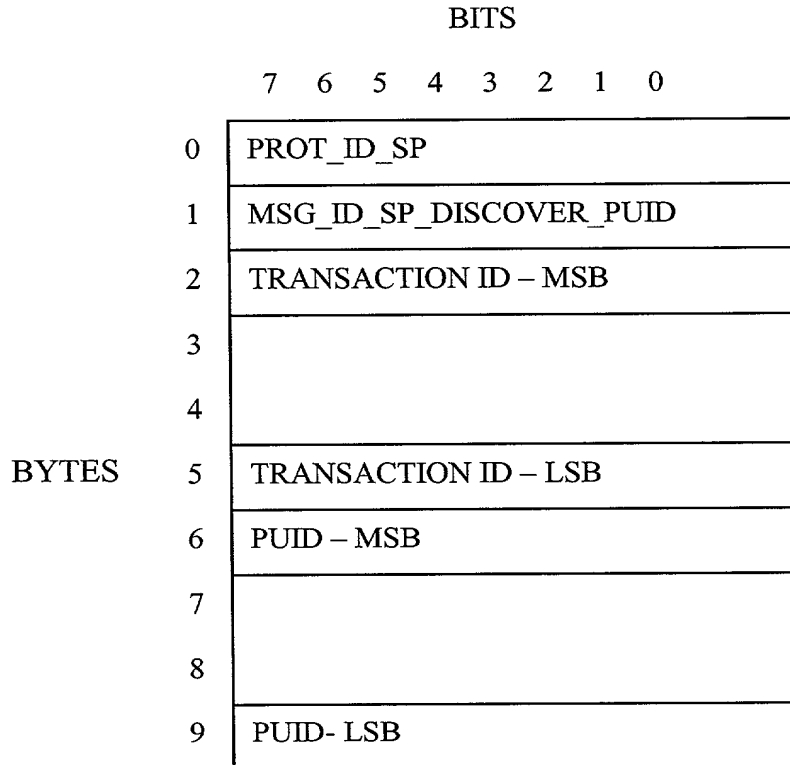


FIG. 7

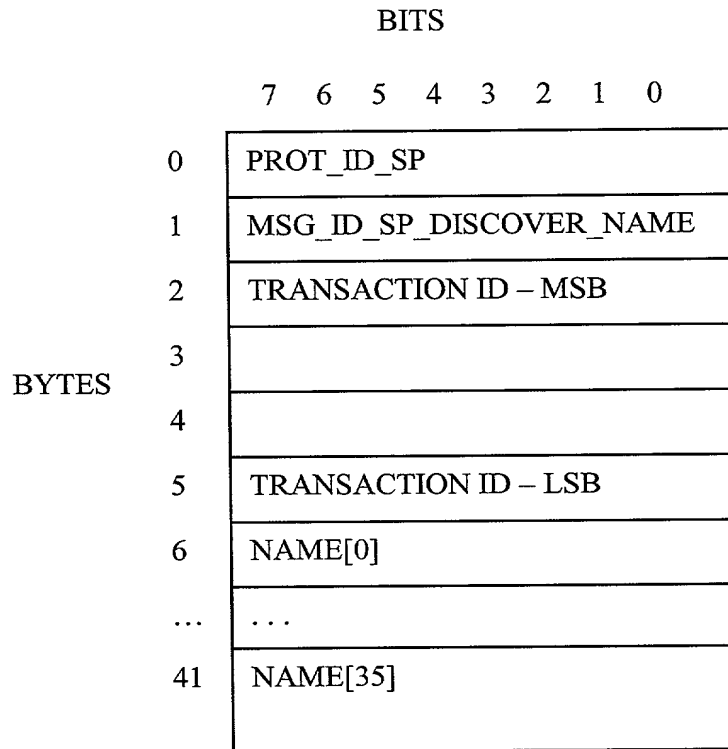


FIG. 8

FIG. 8

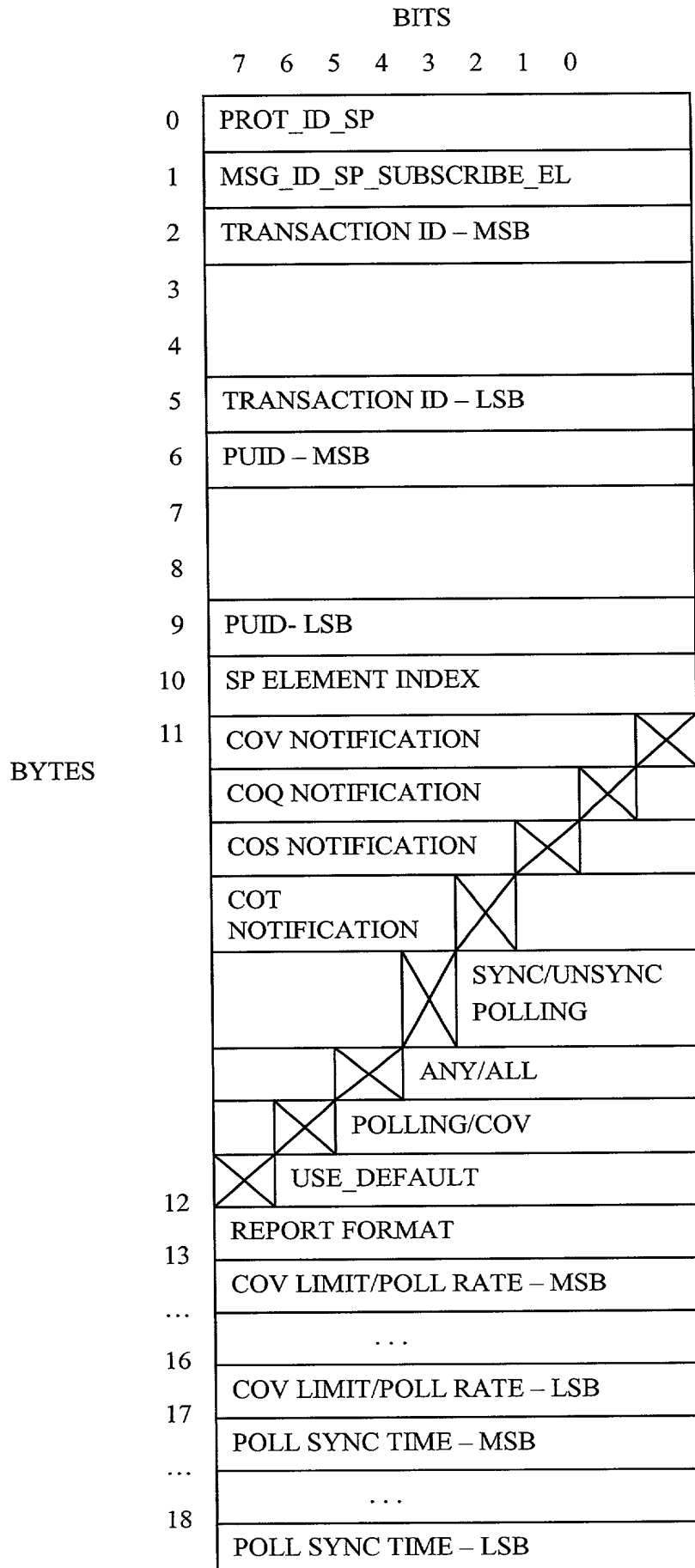


FIG. 9

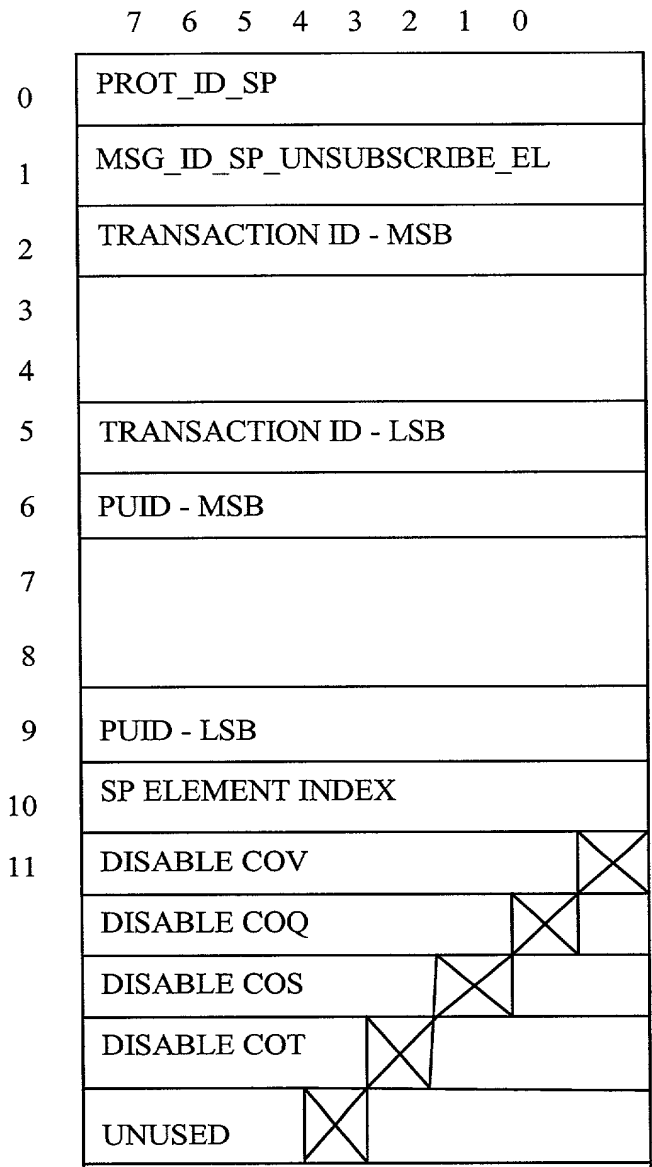


FIG. 10

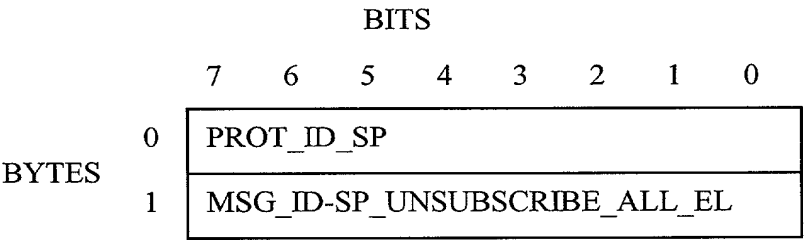


FIG. 11

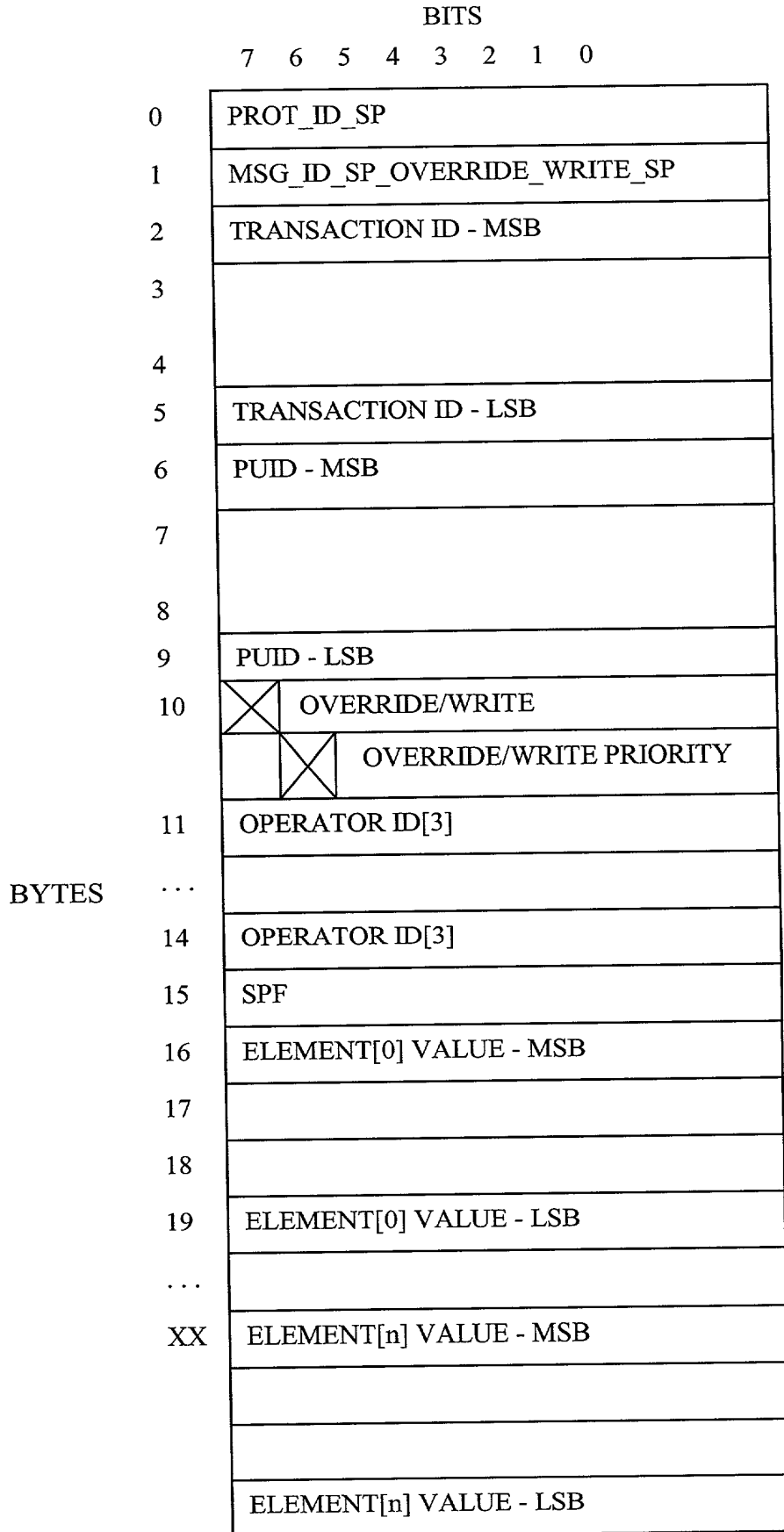


FIG. 12

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BYTES



		BITS							
		7	6	5	4	3	2	1	0
0	PROT_ID_SP								
1	MSG_ID_SP_RELEASE								
2	TRANSACTION ID - MSB								
3									
4									
5	TRANSACTION ID - LSB								
6	PUID - MSB								
7									
8									
9	PUID - LSB								
10	 OVERRIDE/WRITE								
	 OVERRIDE/WRITE PRIORITY								
11	OPERATOR ID[0]								
...									
14	OPERATOR ID[3]								

FIG. 13

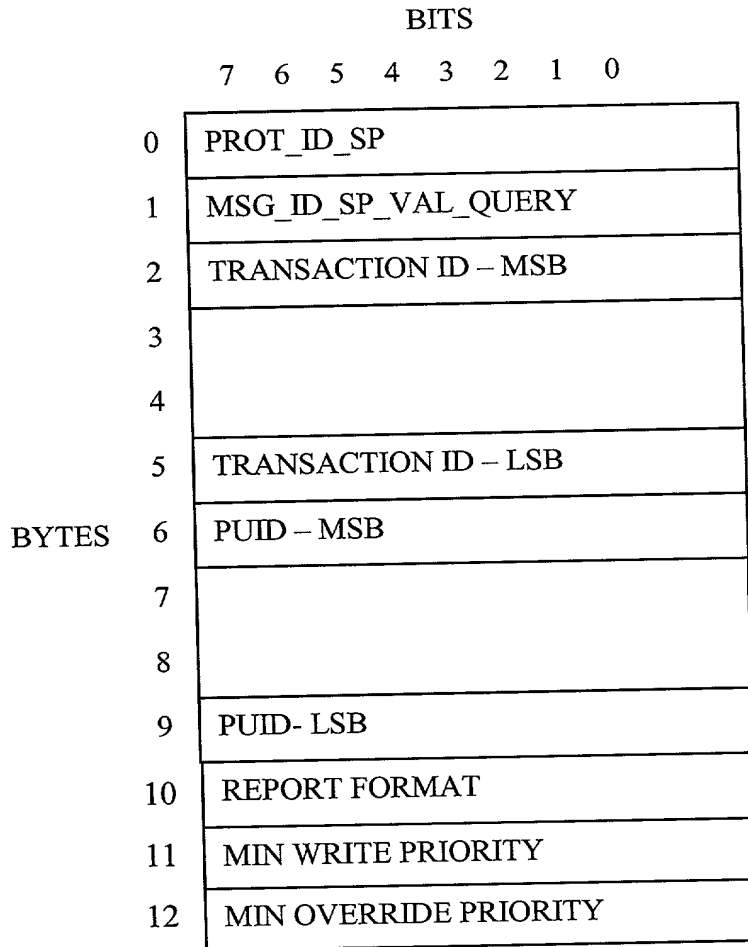


FIG. 14

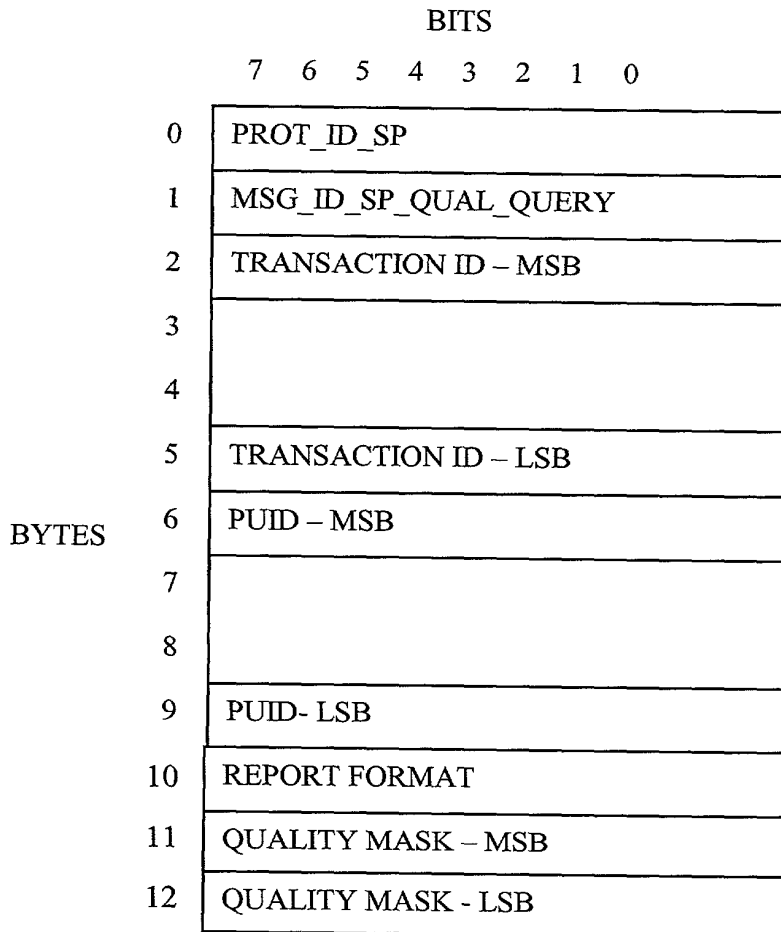


FIG. 15

FIG. 16

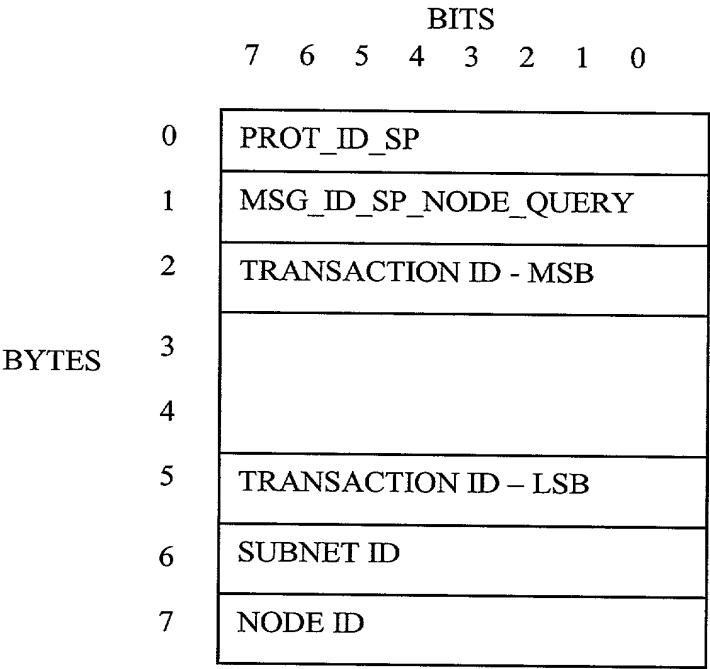


FIG. 16

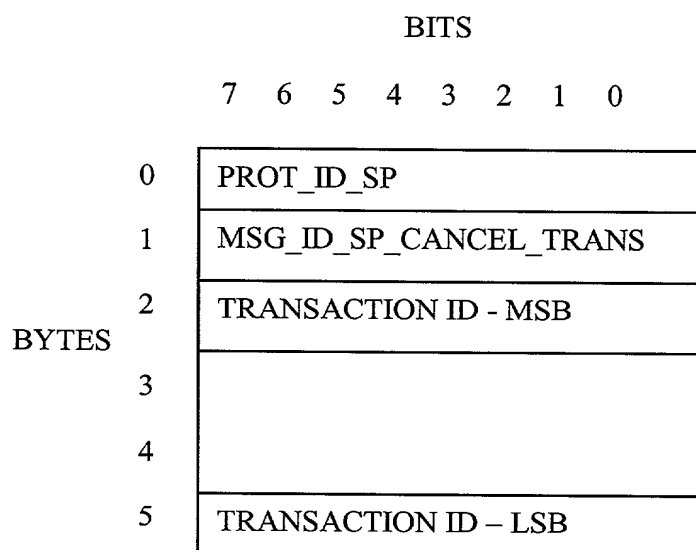


FIG. 17

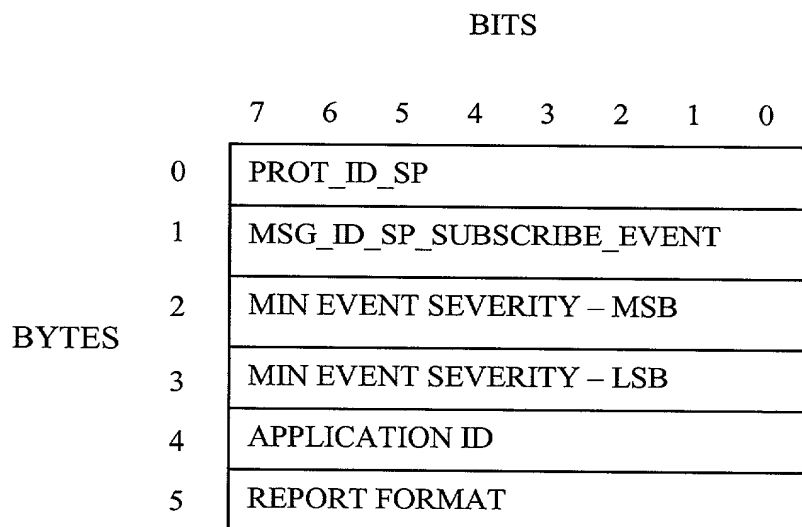


FIG. 18

FIG. 17

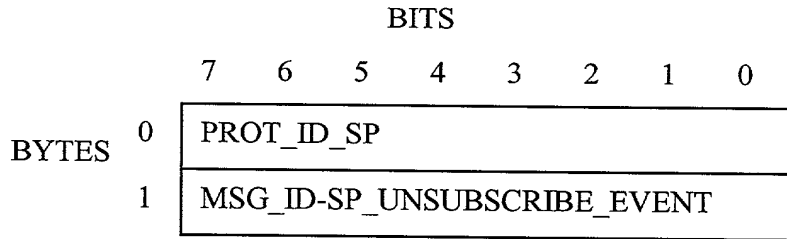


FIG. 19

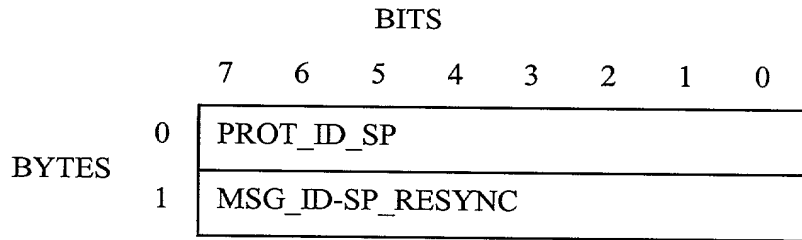


FIG. 20

FIG. 19

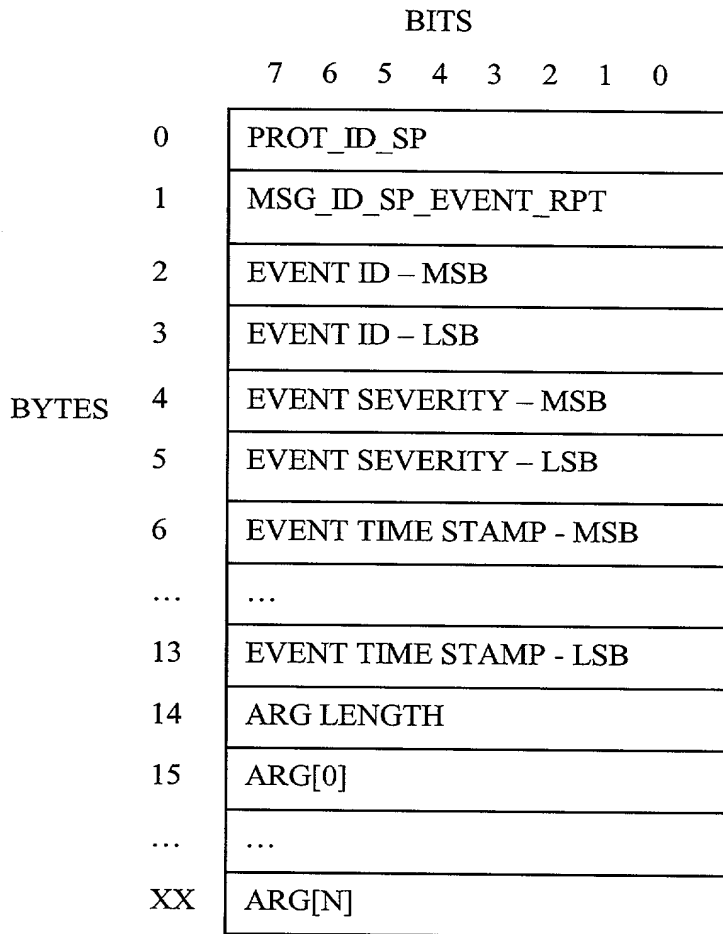


FIG. 21

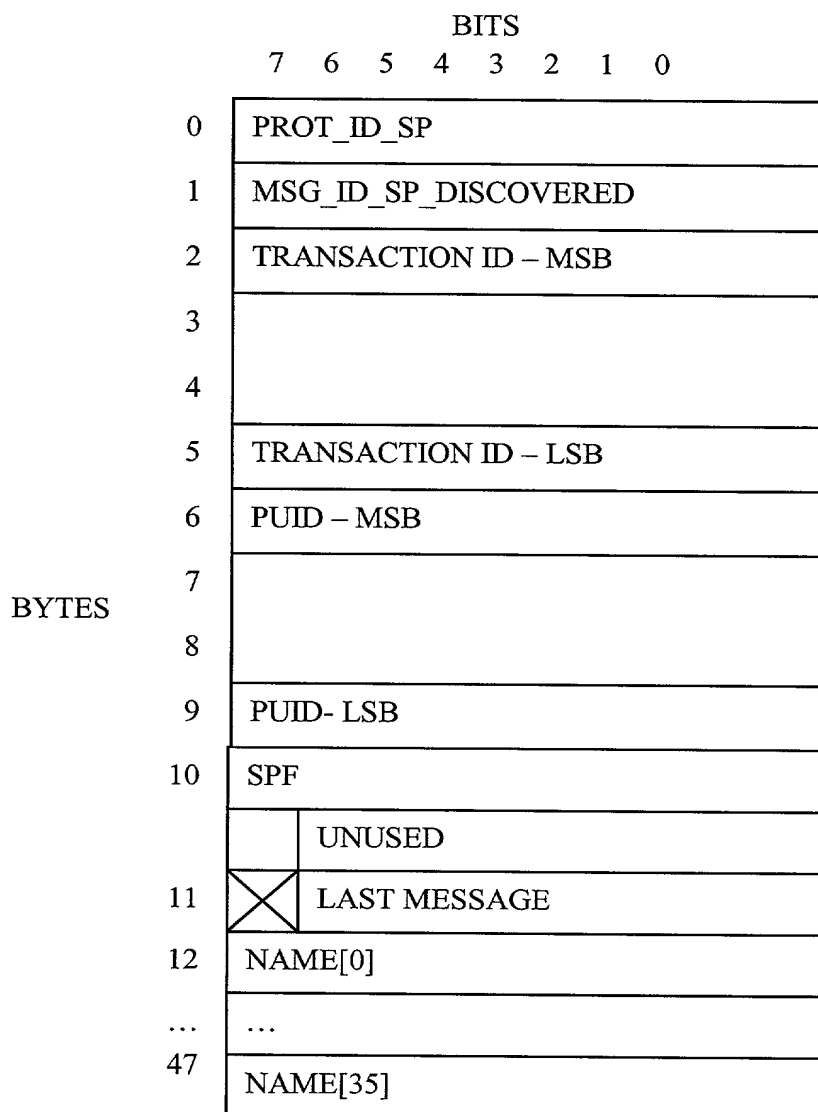


FIG. 22

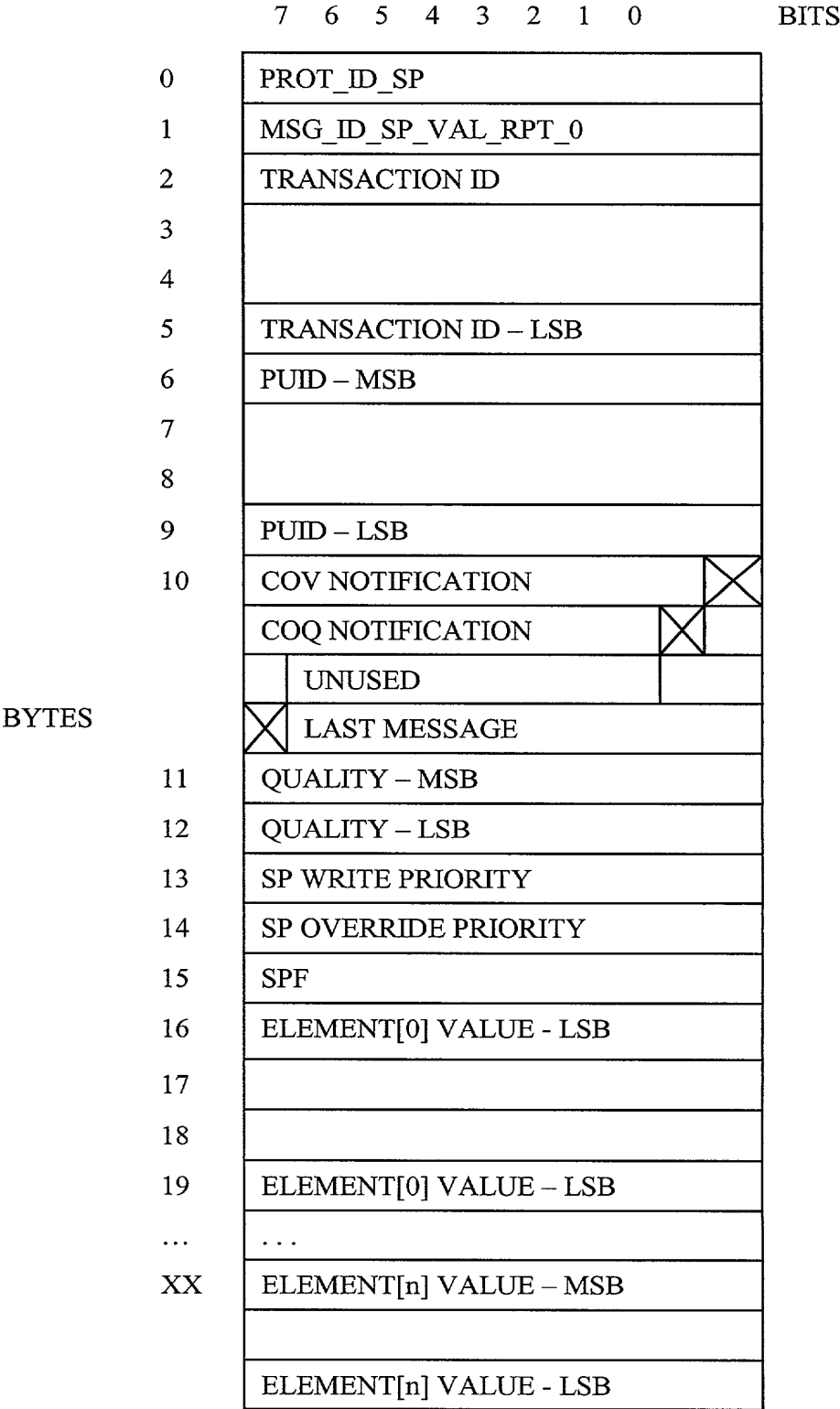


FIG. 23

FIG. 24

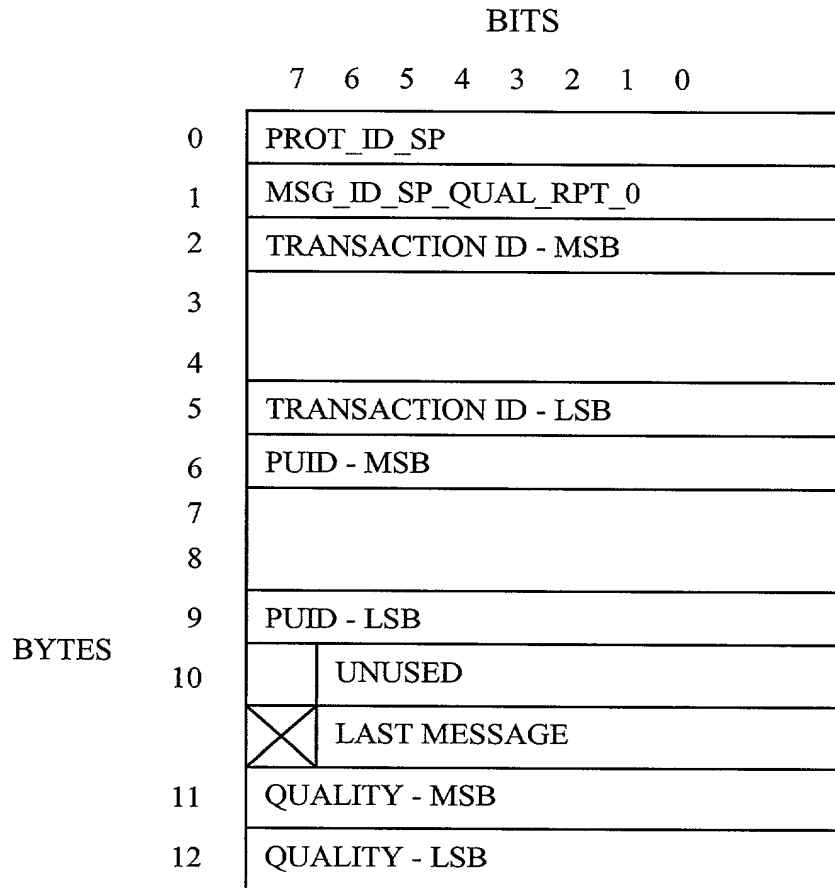


FIG. 25

FIG. 26

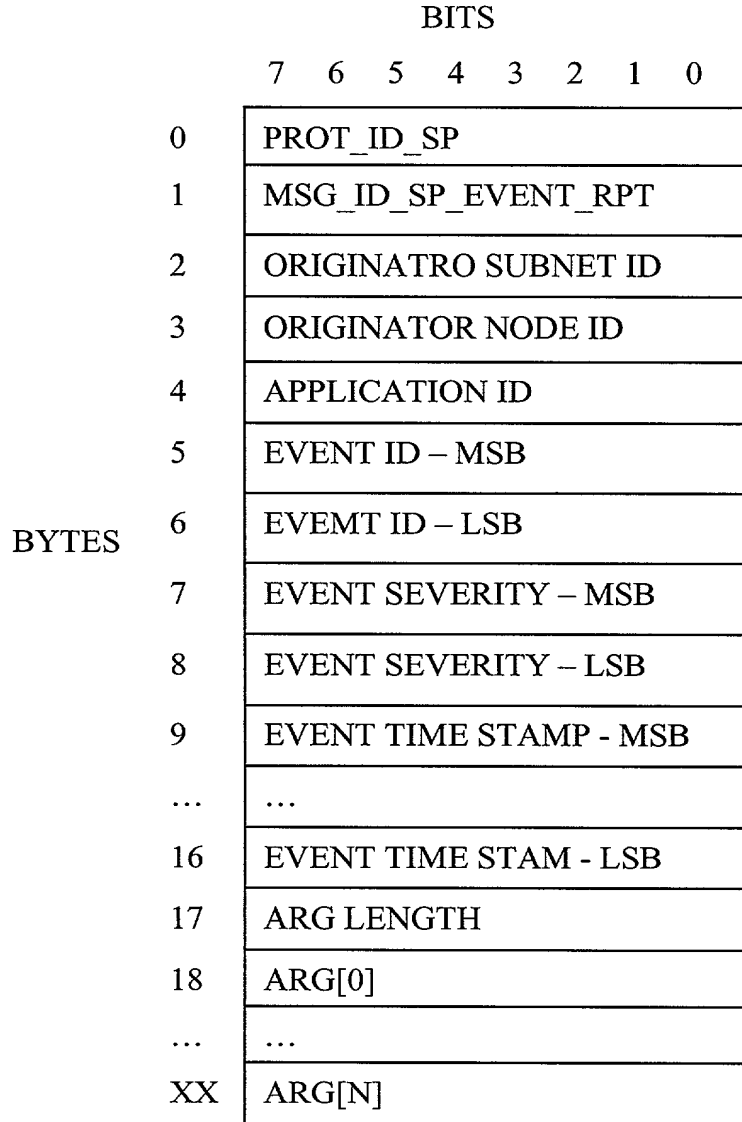


FIG. 27

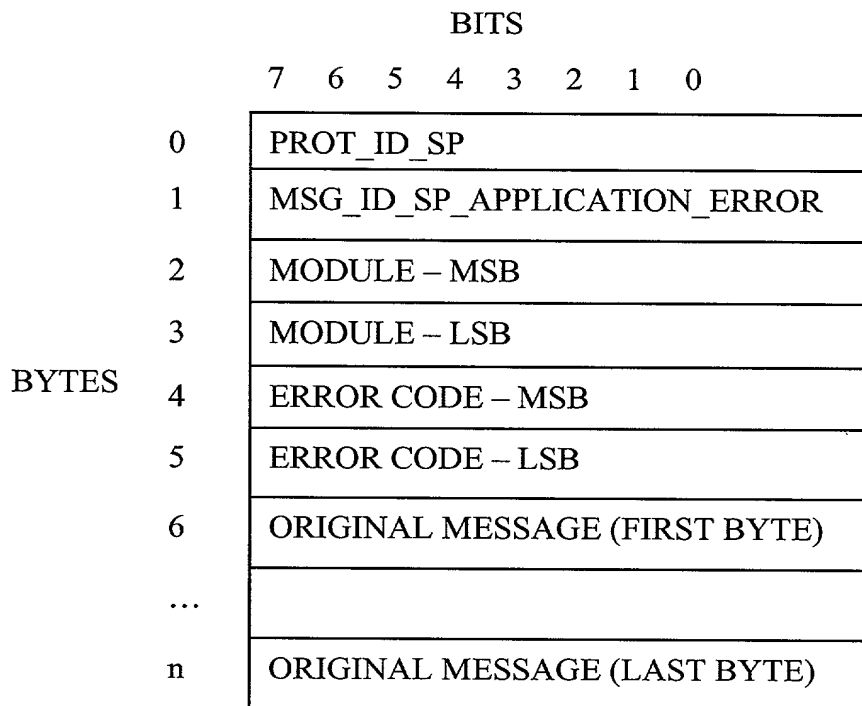


FIG. 28